



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Katsushi NAGABA et al.

Serial No: 09/965,534

Filed: September 25, 2001

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE

Art Unit: 2826

Examiner: Fetsum Abraham

I hereby certify that this correspondence
is being deposited with the United States
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Anthony J. Orler Reg. No. 41,232

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PETITION FOR EXTENSION OF TIME

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the Commissioner for a two-month extension of time extending to September 2, 2003, the period for response to the final Office Action dated April 2, 2003. A check for \$410 is enclosed. The responsive paper(s) are attached.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of this petition is enclosed.

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By:

Anthony J. Orler

Registration No. 41,232

Attorney for Applicant(s)

Date: September 2, 2003

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